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Art Unit 1765

SEP 0 5 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Hariprasad Sreedharamurthy et al.

Serial No. 10/039,459

Filed November 7, 2001

Confirmation No. 1061

APPARATUS AND PROCESS FOR THE PREPARATION OF LOW-IRON SINGLE CRYSTAL SILICON SUBSTANTIALLY FREE OF AGGLOMERATED INTRINSIC POINT DEFECTS

September 5, 2002

TO THE COMMISSIONER FOR PATENTS,

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SEP 9 - 2002 TC 1700

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. 1.97 and 1.98 and MPEP 609, and in compliance with the duty of disclosure set forth in 37 C.F.R. 1.56, applicants submit copies of the references listed on the attached PTO/SB/08A for consideration by the Patent and Trademark Office in the above-entitled application and to be made of record therein.

I certify that each item contained in this statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. Enclosed is a copy of the PCT International Search Report, dated August 7, 2002 in which these references were cited.

Respectfully submitted,

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